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7-25-05

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

JFW

Applicant: Calder et al.

Serial No.: 10/659,066

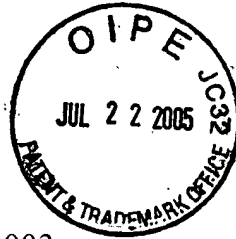
Conf. No.: 3820

Filed: September 9, 2003

For: METHOD AND APPARATUS
FOR IDENTIFYING SIMILAR
REGIONS OF A PROGRAM'S
EXECUTION

Art Unit: 2191

Examiner: Steelman, Mary J.



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July 22, 2005
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INFORMATION DISCLOSURE STATEMENT

Mail Stop AMENDMENT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

This IDS is submitted under 37 C.F.R. §1.97(b) within either of the following time periods, whichever occurs last:

- (a) within three months of either the filing date of the national application or the date of entry into the national stage; or
- (b) before the mailing date of first office action on the merits (i.e., not including actions such as restriction requirements).

Applicant(s) submit herewith Form PTO-1449 (Information Disclosure Citation) together with copies of patents, publications or other information of which applicant(s) are aware, which applicant(s) believe may be material to the examination of this application and for which there may be a duty to disclose in accordance with 37 C.F.R. §1.56. Applicant(s) respectfully submit that the citation of any reference on Form PTO-1449 does not constitute an admission that the reference qualifies as prior art.

It is requested that the information disclosed on the enclosed Form PTO-1449 be made of record in this Application.

The Commissioner is hereby authorized to charge any additional fees which may be required to this application under 37 C.F.R. §§1.16-1.17, or to credit any overpayment, to Deposit Account No. 07-2069. A duplicate copy of this sheet is enclosed.

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July 22, 2005

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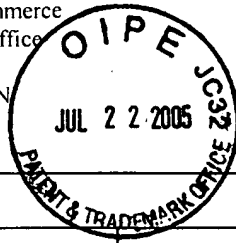
Respectfully submitted,
GREER, BURNS & CRAIN, LTD.

By:

Arik B. Ranson

Registration No. 43,874

Form PTO-1449 U.S. Department of Commerce
(Rev. 8-88) Patent and Trademark Office



Attorney Docket No.: 1201.68199

Serial No.: 10/659,066

INFORMATION DISCLOSURE CITATION
(Use several sheets if necessary)

Applicant: Calder et al.

Filing Date: September 9, 2003

Group: 2191

U.S. PATENT DOCUMENTS

Examiner Initial*	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate

FOREIGN PATENT DOCUMENTS

	Document Number	Date	Country	Class	Subclass	Translation	
						Yes	No

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	Aragon, J.L., Gonzales, J., Gonzales, A., "Power-Aware Control Speculation Through Selective Throttling," Proceedings of the 9 th International Symposium on High-Performance Computer Architecture, February 2003.
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	Dhodapkar, A., Smith, J.E., "Comparing Program Phase Detection Techniques," Proc. of the Int. Symposium on Microarchitecture, 2003.

Examiner

Date Considered

*Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Form PTO-1449 U.S. Department of Commerce (Rev. 8-88) Patent and Trademark Office	Attorney Docket No.: 1201.68199	Serial No.: 10/659,066
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	Dhodapkar, A.S., Smith, J.E., "Managing Multi-Configuration Hardware via Dynamic Working Set Analysis," 29 th International Symposium on Computer Architecture (ISCA 2002), Alaska, May 2002.
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